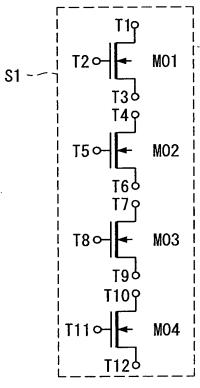


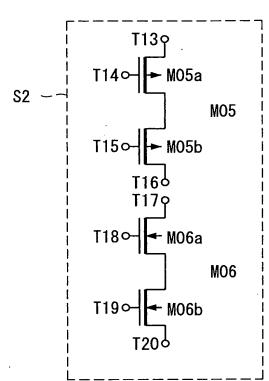
FIG. 1A

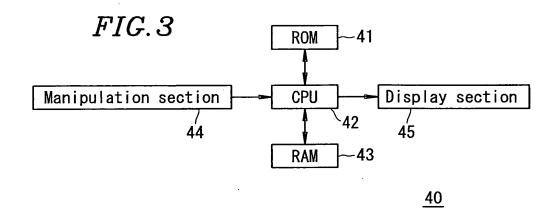
Gate electrode Gate electrode Source-drain region Source-drain region M01 i M05 M05a--M05b M02 | MO3 M06 M06a M06b MO4 ! **S2** 

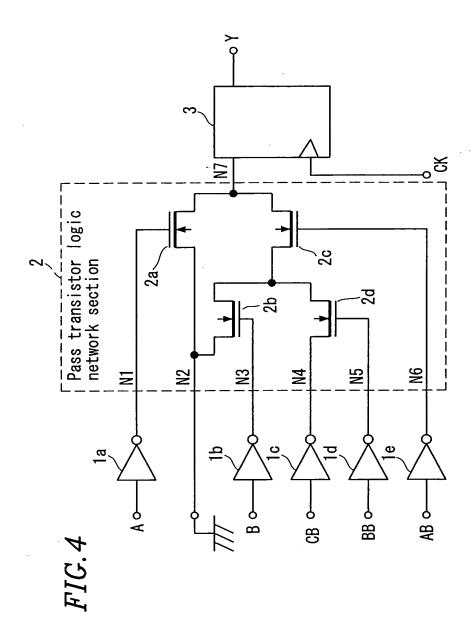
FIG.2A

FIG.2B









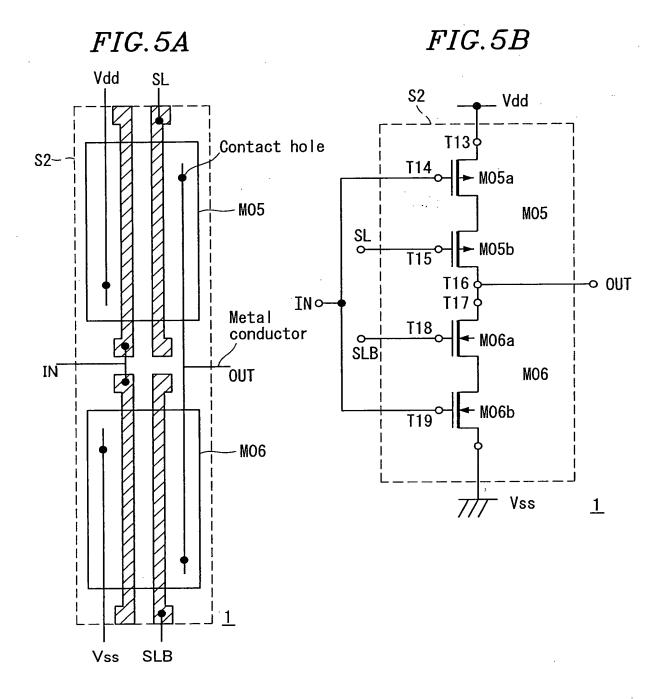
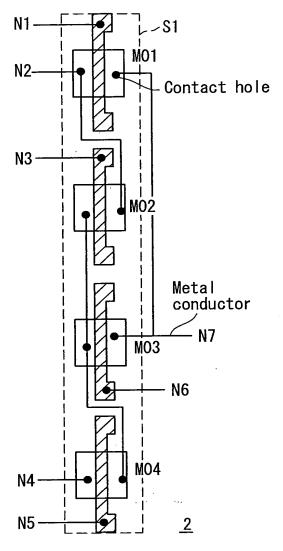
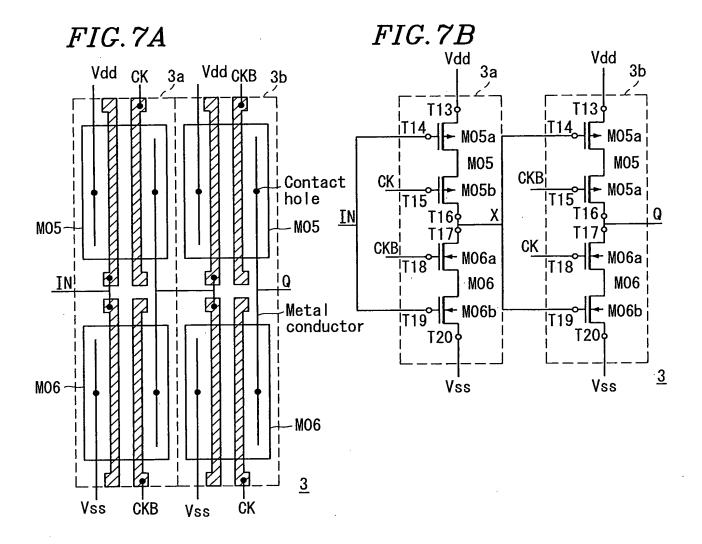
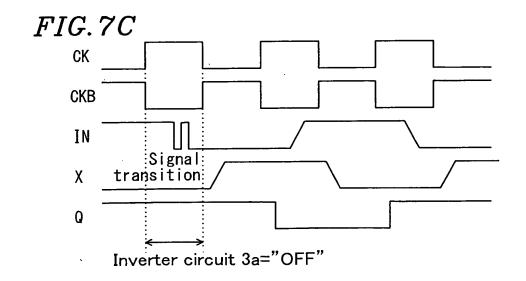


FIG. 6







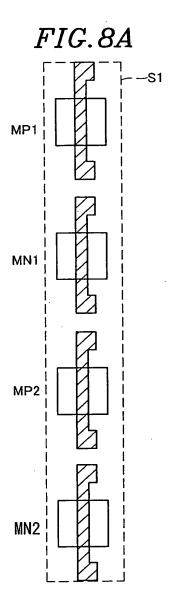


FIG. 8B

TP10 --S1

TP20 - MP1

TP30

TN10

TN20 - MN1

TN30

TP40

TP50 - MP2

TP60

TN40

TN50 - MN2

TN60

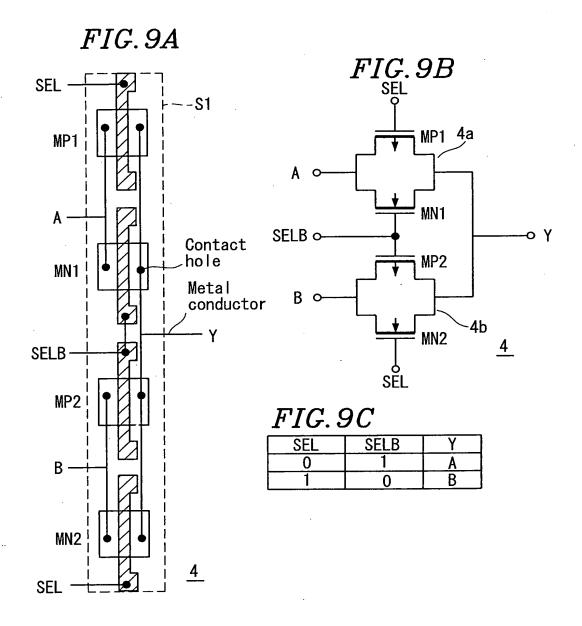
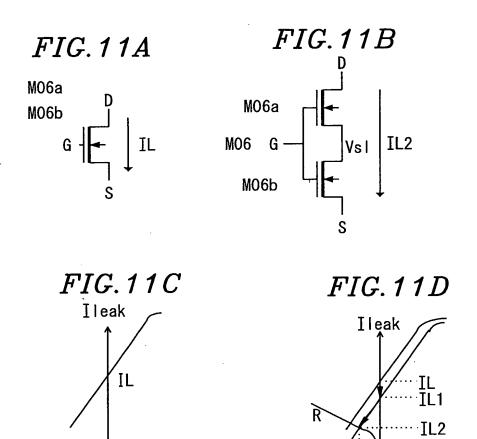


FIG. 10A Vdd SL *FIG. 10B* – Vdd Contact hole T13 T14 - M05a M05 M05 - M05b Metal conductor ~0UT IN 0-IN-- OUT T18 - M06a M06 M06b SLB! T20 ¢ -M06 5 Vss <u>5</u> Vss SLB

→ Vgs



→ Vgs

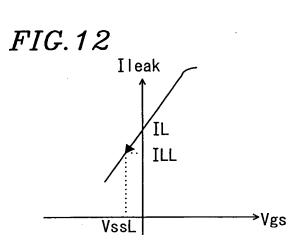


FIG. 13A

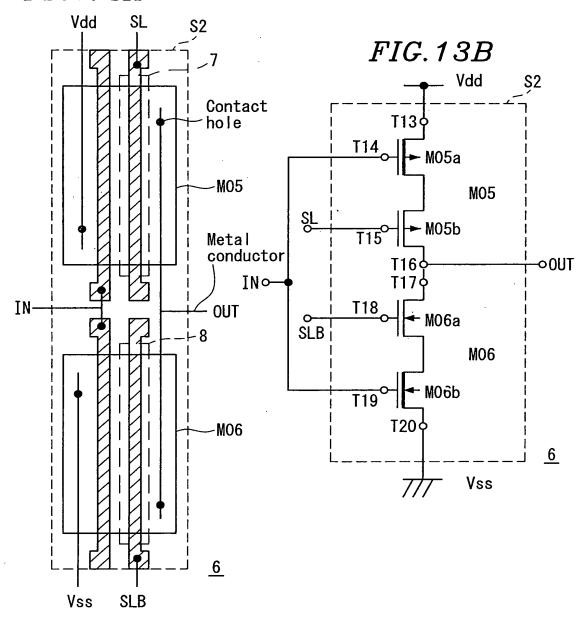


FIG. 14A

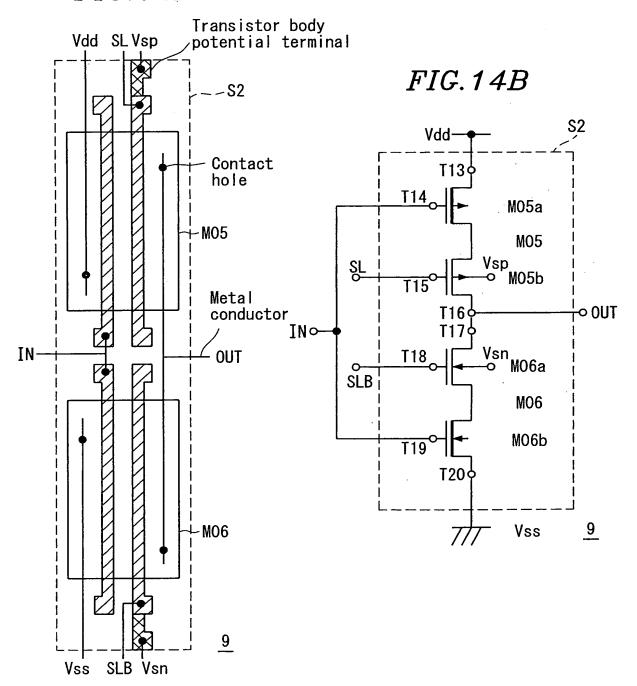


FIG. 15A

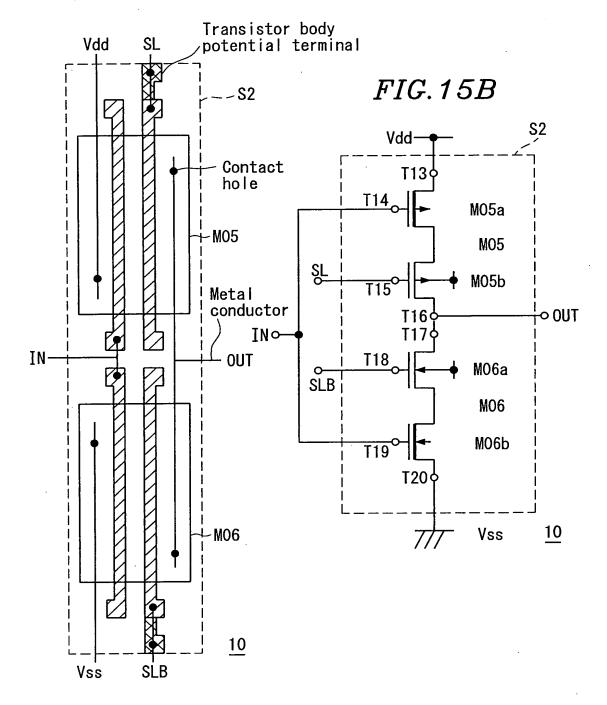
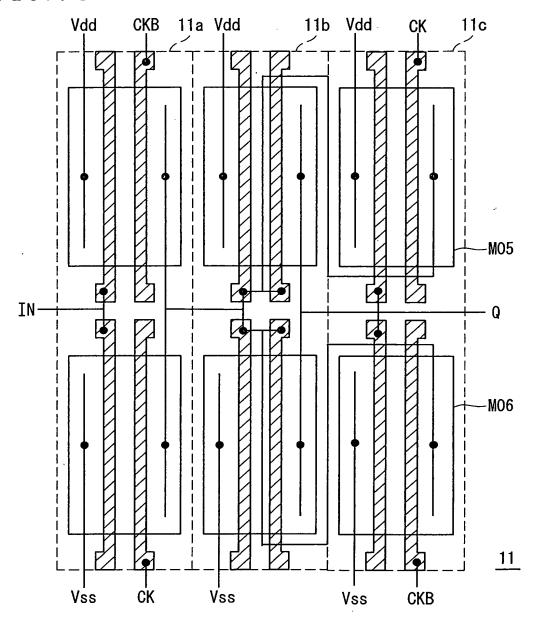


FIG. 16



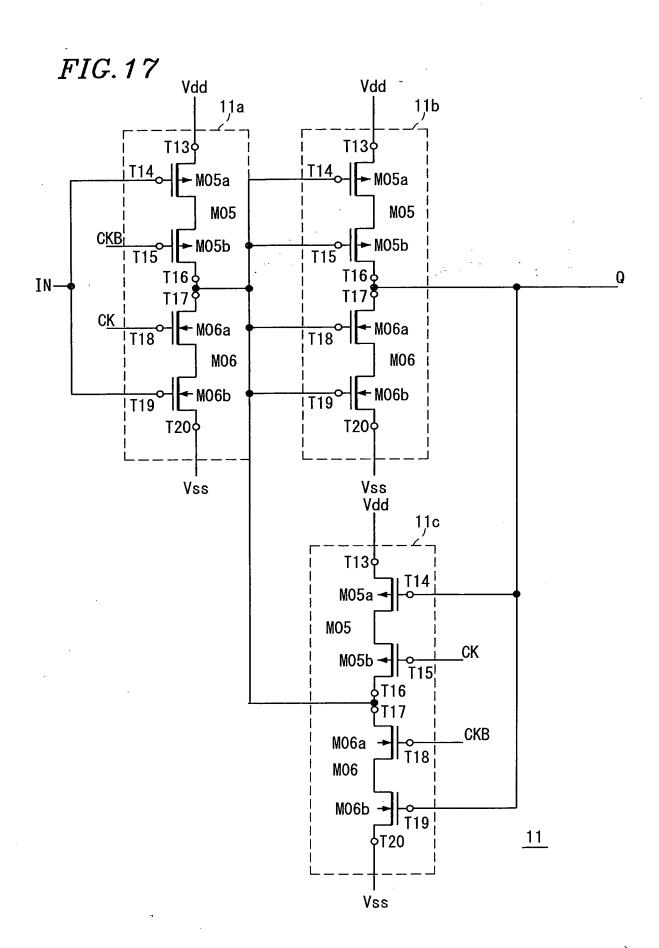


FIG. 18

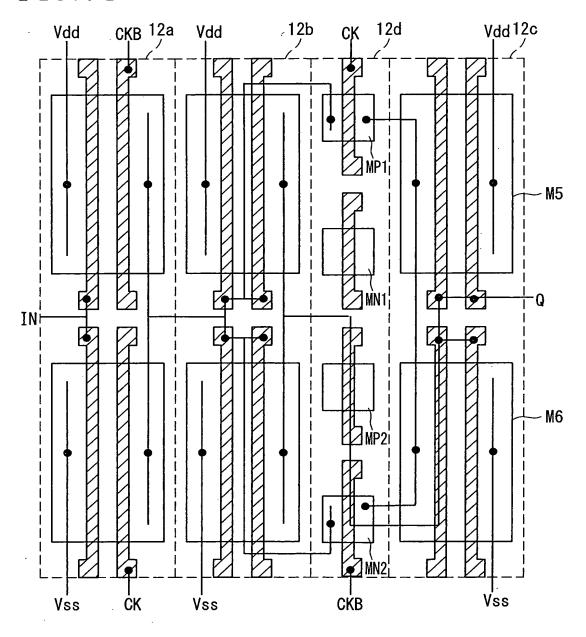


FIG. 19

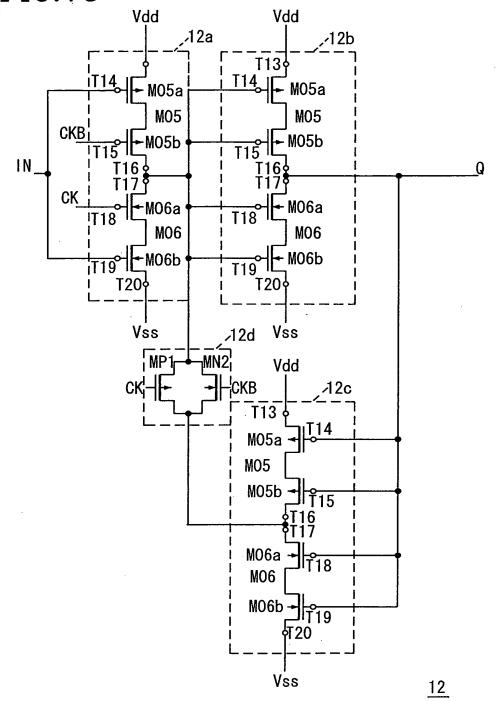


FIG.20

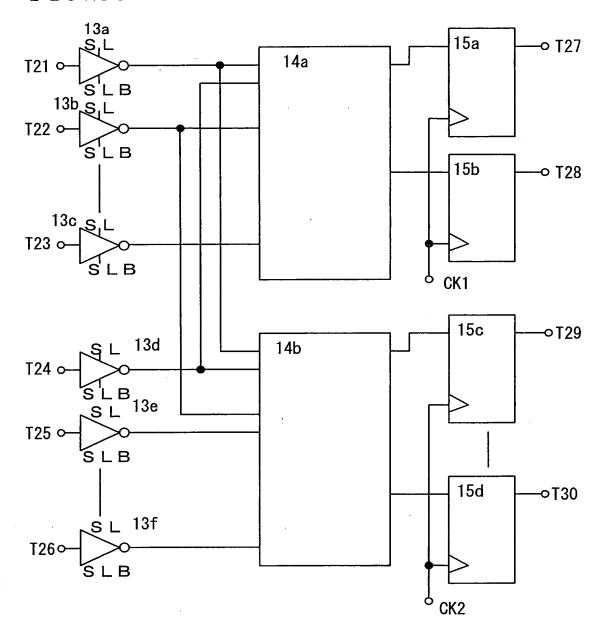
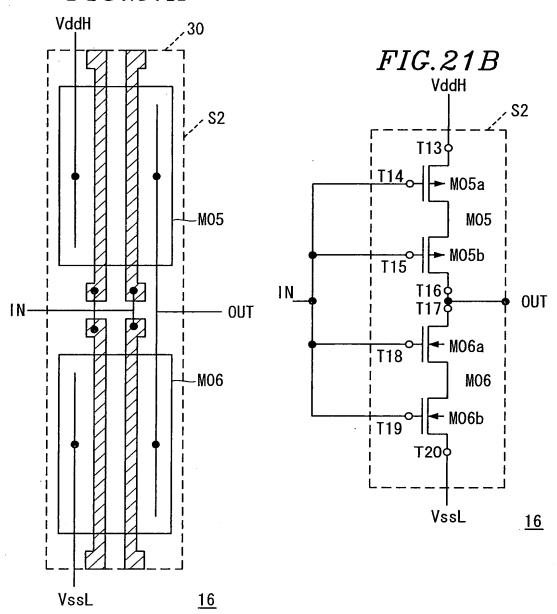


FIG.21A



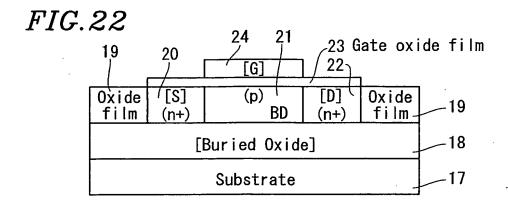
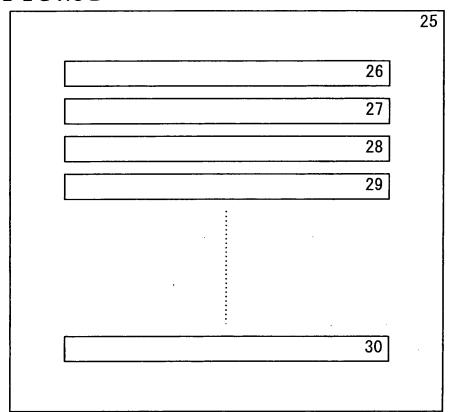


FIG.23



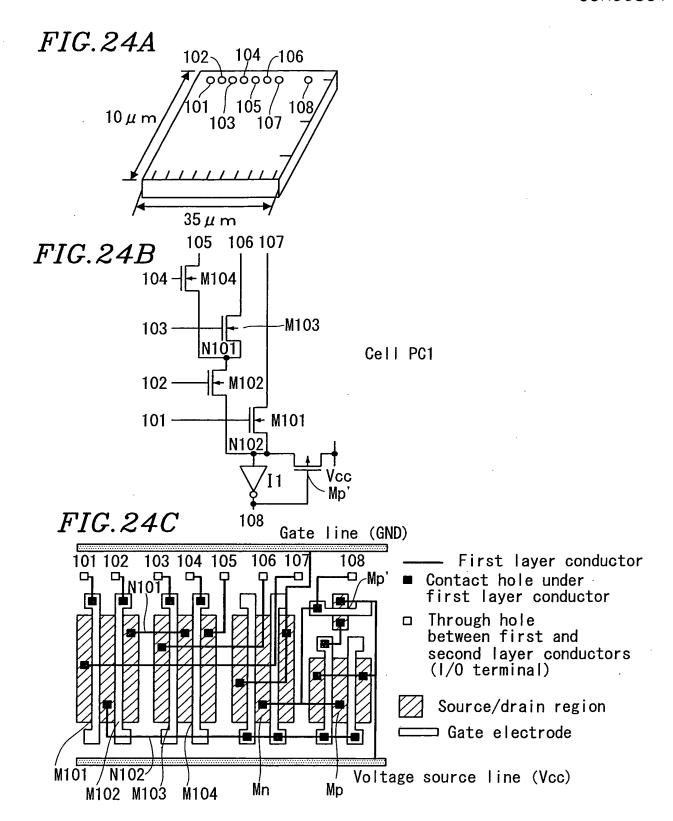
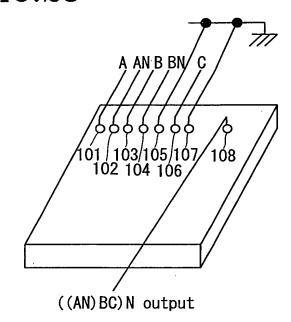


FIG.25



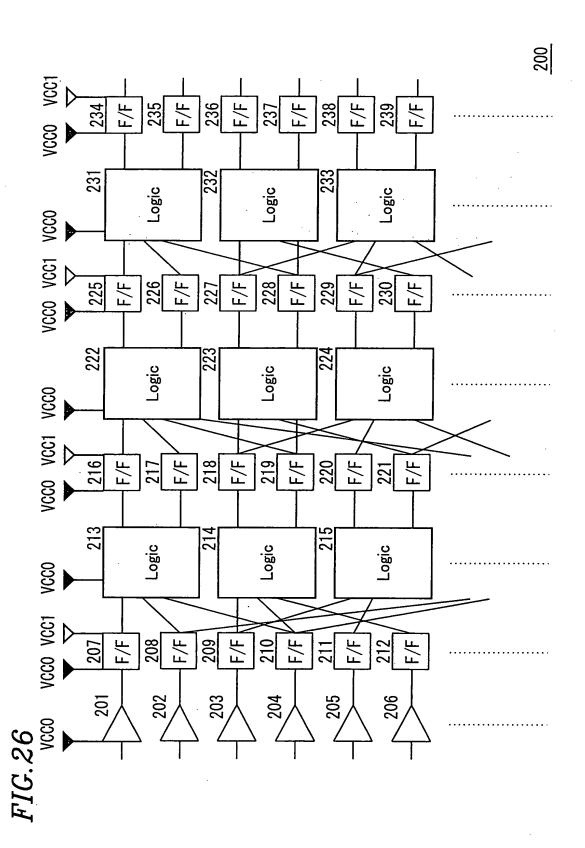


FIG.27

